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IN THE

UNITED STATES PATENT AND TRADEMARK OFFICE

inventor(s).

Seok-Jun WON

Serial No.:

10/676,865

Examiner:

Loke, Steven Ho Yin

Filing Date:

September 30, 2003

Group Art Unit: 2811

Title:

FLAT-TYPE CAPACITOR FRO INTEGRATED CIRCUIT AND METHOD OF

MANUFACTURING THE SAME

Confirmation No.

7630

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

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Sir:								
	This Information Disclosure Statement is submitted:							
	\boxtimes	under 37 CFR 1.97 (b); or						
	or t	(Within three months of filing national application; or date of entry of International application; pefore mailing date of first office action on the merits; whichever occurs last)						
		under 37 CFR 1.97 (c) together with either a:						
	—	Statement under 37 CFR 1.97 (e), or						
		a \$180 fee under 37 CFR 1.17 (p); or						
		(After mailing of first Office Action, but prior to Notice of Allowance or Final Office Action)						
		under 37 CFR 1.97 (d) together with:						
		Statement under 37 CFR 1.97 (e), and						
		a \$180.00 fee set forth in 37 CFR 1.17 (p).						
		(Filed after final action or notice of allowance, whichever occurs first, but before payment of						
the issu	e fee)							
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copies c	f patents	, publications or other information of which applicant(s) are aware, which applicant(s) believe(s)						
	material t CFR 1.56	to the examination of this application and for which there may be a duty to disclose in accordance						
	It is requested that the information disclosed herein be made of record in this application.							

Any deficiency or overpayment should be charged or credited to deposit account number 13-1703. A duplicate copy of this sheet is enclosed.

Customer No. 20575

Respectfully submitted,

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop Amendment Commissioner for Patents, P.O. Box 1450,

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NOV 0 8 2004 Appargant:

PATENT APPLICATION

Seok-Jun WON

Serial No.

10/676,865

September 30, 2003

Group:

2811

Title:

FLAT-TYPE CAPACITOR FOR INTEGRATED CIRCUIT AND METHOD OF MANUFACTURING THE SAME

INFORMATION DISCLOSURE CITATION FORM PTO-1449 (Modified)

U.S. PATENT DOCUMENTS

Exam <u>Init</u>	<u>Ref</u>	Document Number	Issue <u>Date</u>	<u>Name</u>	<u>Class</u>	Sub <u>Class</u>			
		6,271,596	Aug. 7, 01	Alers					
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		2000-228497	Aug. 15, 00	Japan	Lee Hyae-Ryoung				
OTHER DOCUMENTS									
Exam <u>Init</u>									
	English Language of Abstract for Japanese Patent Publication No. 2000-228497								
Examiner:									
Date Considered:									